IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Yasutoshi AIBARA et al.

Appln. No.:

Filed: HEREWITH

For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND IMAGING

SYSTEM

PRELIMINARY AMENDMENT

Assistant Commissioner for Patents Washington, D.C. 20231

Sir:

Prior to examination, please amend the above-identified patent application as indicated below.

IN THE CLAIMS:

- 9. (Amended) An imaging system according to any one of
- 2 claims 5 to 7, wherein a storage means is provided to store a

- 3 digital video data, said semiconductor integrated circuit
- 4 device for image process is provided with a data compression
- 5 circuit for compressing the code converted by said second code
- 6 conversion means and a data expanding circuit for expanding
- 7 the compressed data, and the data compressed by said data
- 8 compression circuit is stored in said storage means.

Please add the following claim:

- 11. (New) An imaging system according to claim 8,
- 2 wherein a storage means is provided to store a digital video
- 3 data, said semiconductor integrated circuit device for image
- 4 process is provided with a data compression circuit for
- 5 compressing the code converted by said second code conversion
- 6 means and a data expanding circuit for expanding the
- 7 compressed data, and the data compressed by said data
- 8 compression circuit is stored in said storage means.

REMARKS

Claim 9 has been amended to eliminate a dependency that does not conform with U.S. practice. New Claim 11 corresponds to the dependency eliminated from Claim 9.

The Commissioner is hereby authorized to charge to

Deposit Account No. 50-1165 any fees under 37 C.F.R. §§ 1.16

and 1.17 that may be required by this paper and to credit any

overpayment to that Account. If any extension of time is

required in connection with the filing of this paper and has

not been requested separately, such extension is hereby

requested.

Respectfully submitted,

Reg. No. 31,568

MWS:lmb

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Marked-up copy of Claim -- AIBARA et al.

9. (Amended) An imaging system according to any one of claims 5 to [8] 7, wherein a storage means is provided to store a digital video data, said semiconductor integrated circuit device for image process is provided with a data compression circuit for compressing the code converted by said second code conversion means and a data expanding circuit for expanding the compressed data, and the data compressed by said data compression circuit is stored in said storage means.